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VLSI

DNA

DNA

نهفته

DNA

mRNA

mRNA

DNA

(G) guanine (A) adenine
base

(T) thymine (C) cytosine
base

G C T A

base . DNA

[, ,] DNA

base

[]

[]

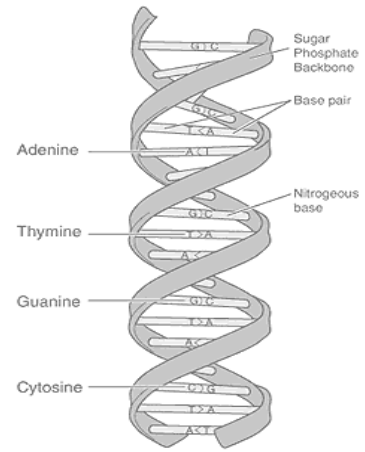
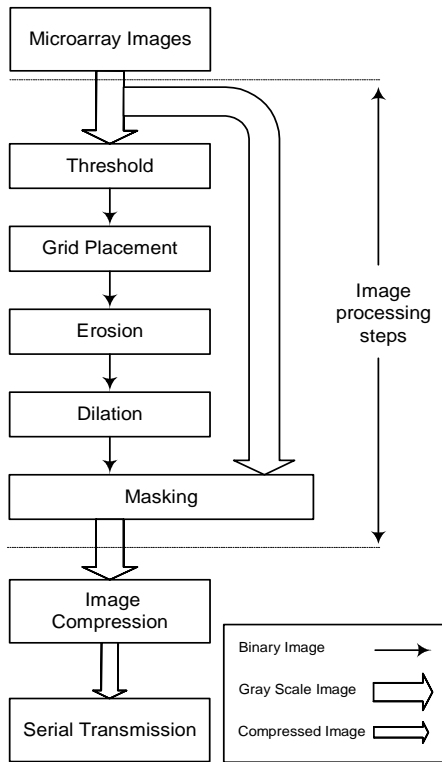
DNA

AIDS SARS
[]
Real-Time

Real- Real-Time

[,]

Time



DNA

base

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[,]

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CNN-UM

Spot

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Arrayer . Arrayer

Spot

Spot

[, ,]

/ * /

[,]

(/ /) mRNA

Spot

cDNA

mRNA

)

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cDNA

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” “

Spot

CCD

Spot

: **Grid**

Spot

Spot

Spot

Spot

Spot

[, , ,]

Grid

Spot

Grid

AND

Grid

Spot

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: **Spot**

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Spot

Spot

Spot

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Spot

Dilation Erosion

Erosion

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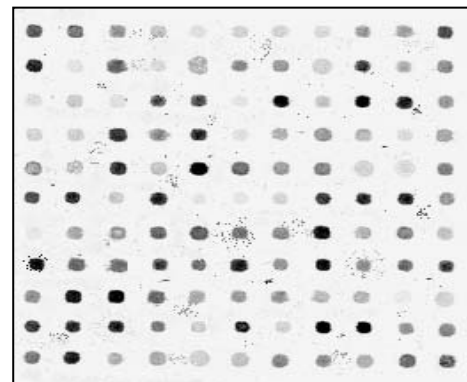
Spot

Spot

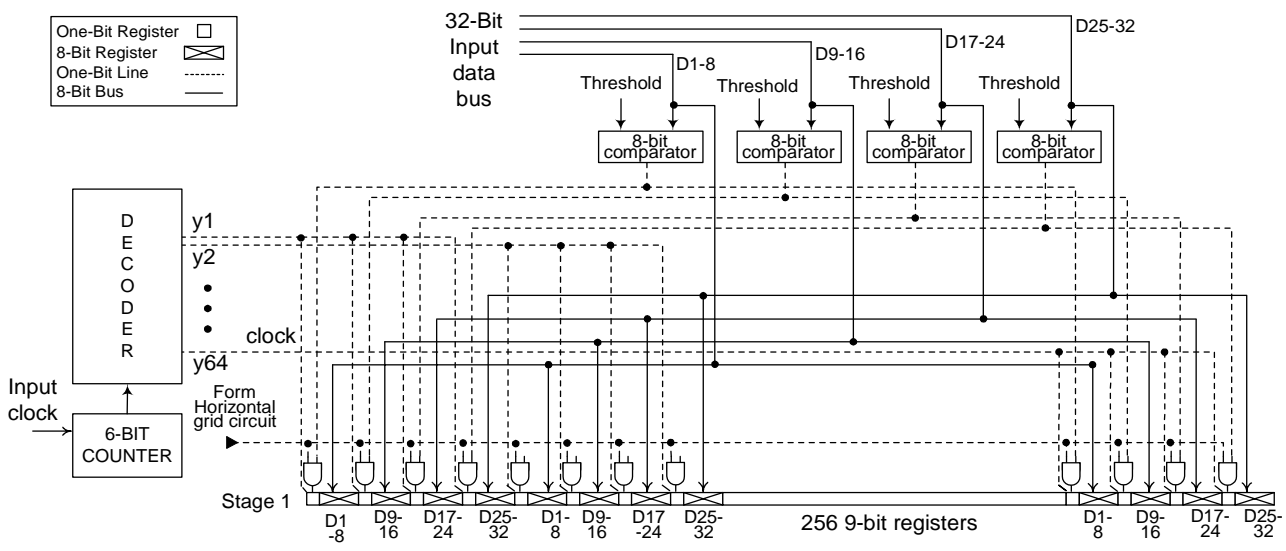
,Dilation

Grid

: **Masking**



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AND

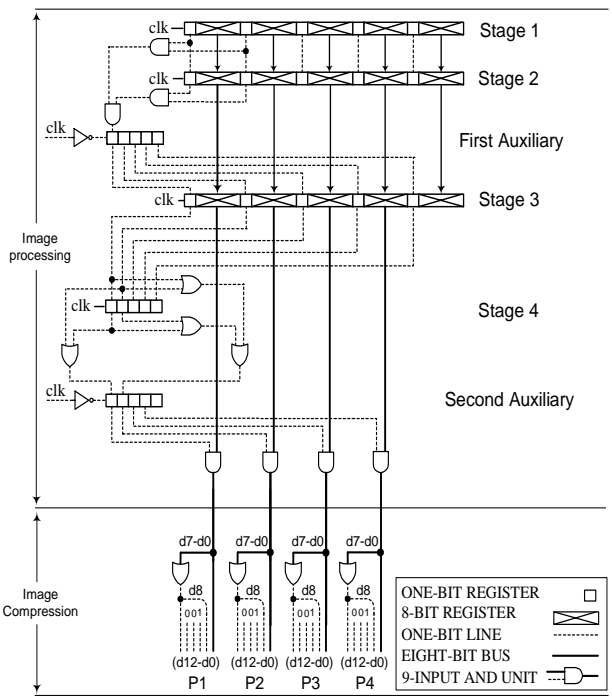
Spot

Spot

Spot

Dilation Erosion

Erosion



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Grid

,Grid

Grid

AND

Grid

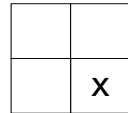
Grid

Grid

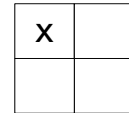
$j+1$

Erosion

j



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Dilation (), Erosion ()

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Spot

OR

Dilation

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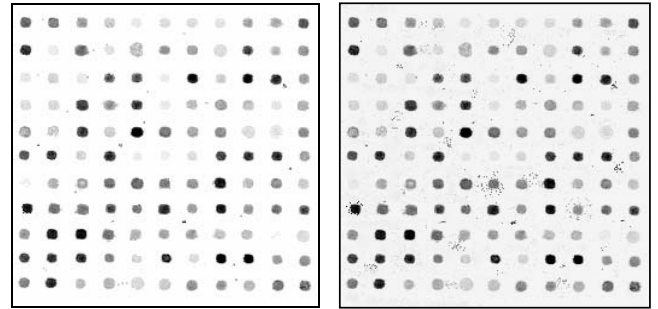
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Spot

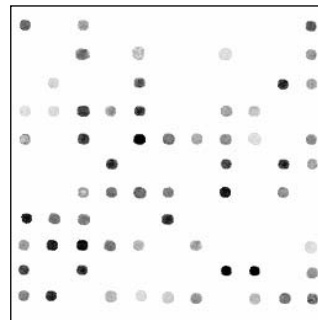
Spot

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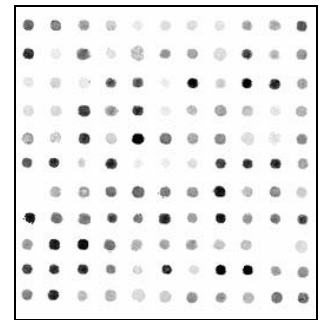


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Dilation

Erosion

Spot

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Dilation

Spot

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AND

AND

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Spot

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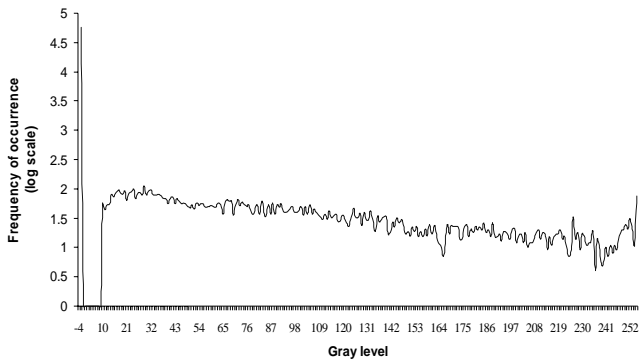
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()-

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Spot

Spot

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(d - d)

(d - d)

Enable

Select

Counter_Busy

" "
" "

" "

Counter_Busy

Counter_Done

OR

(d)

()

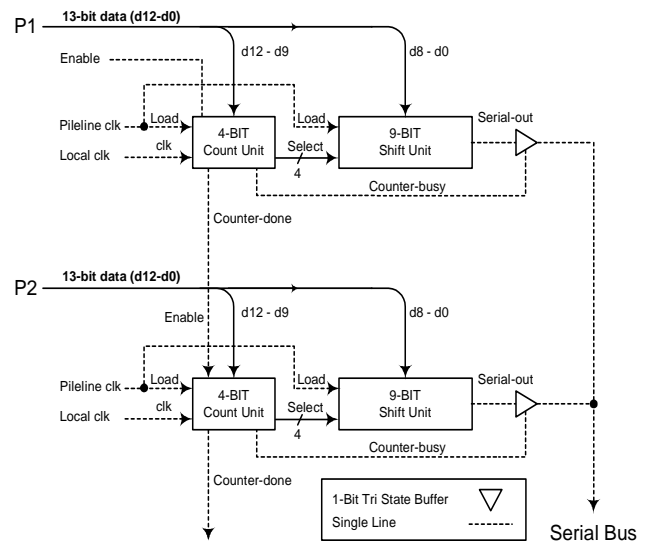
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OR

(d - d)

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(d - d)



$$Speed\ up = \frac{8W}{W + 8NL}$$

Spot

N Spot

L

W

W

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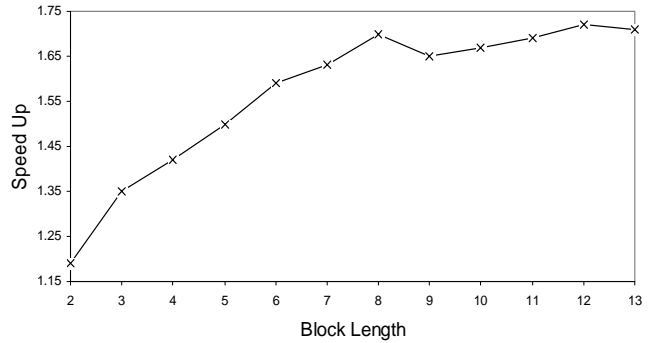
Spot

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W - NL 9NL

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Virtex II FPGA
Xilinx
CLB
FPGA

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FPGA type	Employed CLB Slices	PU (%)	Employed IOBs	PU (%)
2V6000bf975	6627	19.61%	36	5.26%
2V8000bf975	6627	14.22%	36	5.26%
2V10000bf975	6627	10.78%	36	5.26%

PU : Percent of assets being utilized

$O(N)$ $N * N$

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¹ Microarray² Deoxyribonucleic Acid³ Nucleotide⁴ Double Helix⁵ Complementary⁶ Single Strand⁷ Hybridization⁸ Biological Information⁹ Gene Expression¹⁰ Transcription¹¹ Translation¹² Messenger Ribonucleic acid¹³ Yeast¹⁴ Cellular Neural Network Universal Machine¹⁵ Severe Acute Respiratory Syndrome¹⁶ Acquired Immune Deficiency Syndrome¹⁷ Human Genome¹⁸ Pipeline¹⁹ Probe²⁰ Photolithography²¹ Labeling²² Reverse-Transcription²³ Complementary DNA²⁴ Charged Coupled Device²⁵ Gray Scale²⁶ Binary Map²⁷ Grid Placement²⁸ Morphology²⁹ Loss Less Compression methods³⁰ Lossy Compression Methods³¹ Huffman Coding³² Run Length Encoding³³ Vector Quantization³⁴ Transform Coding³⁵ Prefix rule³⁶ Speed up³⁷ Control Logic Block

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() CALTECH

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